·	Subclass	SSIFICATION
	Class	SOUE CLASS

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PATENT DATE

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Thomas Grassl

Nike Tran

PATENT NUMBER

Semiconductor memory chip module

ISSUING CLASSIFICATION CROSS REFERENCE(S) SUBCLASS (ONE SUBCLASS PER BLOCK) INTERNATIONAL CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent				NOTICE OF ALLOWANCE MAILED		
subsequent to(date) has been disclaimed.	(Assistant Exeminar) (Date)		,			
☐ The term of this patent shall						
not extend beyond the expiration date of U.S Patent. No	.0.			ISSUE FEE		1
				Amount Due	Date P	aid
	(Primary Examiner) (Cate)		(Cate)			
The terminalmonths of this patent have been disclaimed.				ISSUE BATCH NUMBER		
	(Legal Instruments Examiner)		(Dene)			

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